

GAB H844

grifo® Analog BLOCK Housing, 8 analog in, 4 opto in, 4 Relays out

GMM AM32

grifo® Mini Modulo AT mega 32L

TECHNICAL MANUAL



grifo®

ITALIAN TECHNOLOGY

Via dell' Artigiano, 8/6
40016 San Giorgio di Piano
(Bologna) ITALY

E-mail: grifo@grifo.it

<http://www.grifo.it>

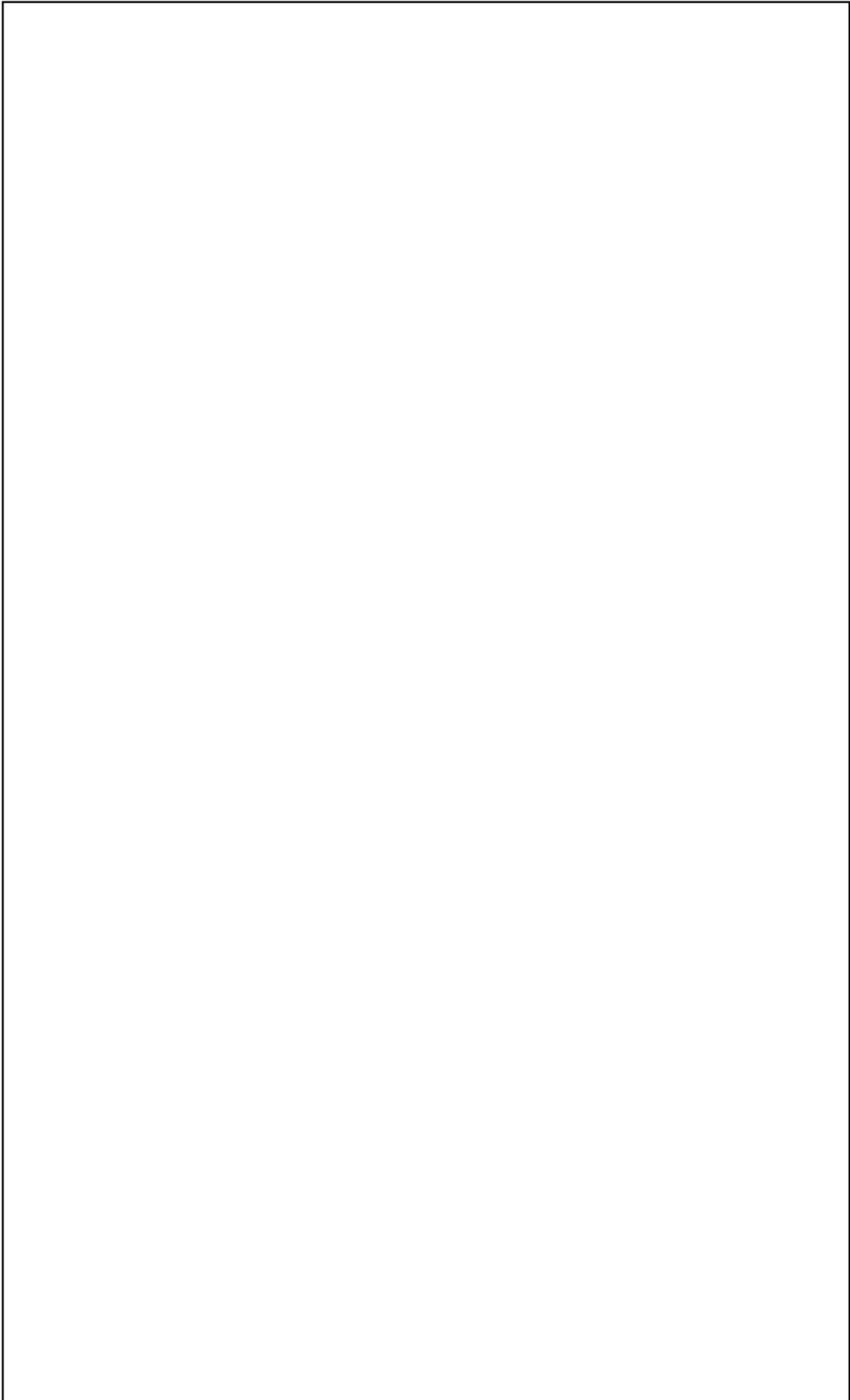
<http://www.grifo.com>

Tel. +39 051 892.052 (a.r.) FAX: +39 051 893.661



GAB H844+GMM AM32 Rel. 5.00 Edition 02 December 2008

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Couple between interface board of **Analog Block GAB H844** series and **Mini Modules** with **AVR** core with **40** pins **GMM AMM32**, able to manage application that involve bot **Analog** and **Digital** signals.

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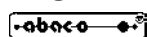
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For specific informations on the components mounted on the card, please refer to the Data Book of the builder or second sources.

SYMBOLS DESCRIPTION

In the manual could appear the following symbols:



Attention: Generic danger

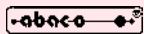


Attention: High voltage



Attention: ESD sensitive device

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COUPLE RESOURCES

The **GAB H844 + GMM AM32** couple has the following resources:

Max. value voltage of A/D converter (Vmv):	2,5 V or 5,0 V
Conditioned analog inputs (0÷20mA, 4÷20 mA, 0÷Vmv, 0÷4*Vmv):	4
Direct analog inputs (0÷Vmv):	0
Relays output:	4
Otpocoupled digital inputs:	4
Buffered TTL digital inputs:	4
TTL multifunction signals:	6
Asynchronous serial line RS 232:	YES
Asynchronous serial line TTL:	YES
Asynchronous serial line RS 422:	YES
Asynchronous serial line RS 485:	YES
Asynchronous serial line Current Loop:	YES
Synchronous serial line I2C BUS:	YES, hardware
CAN interface:	NO
USB interface:	NO
Real Time Clock:	YES

It is important to note that the previous list shows the maximum available resources and some of these ones are not usable in the same time, as described in following figures.

COUPLE CONNECTIONS

In the following tables are reported all connections of all available signals for user of **GAB H844** respect to **GMM AM32** Mini Module. With these connections the user can manage all available resources both in hardware and in software.

If it needed a documentation more detailed, (connection diagram, signal location on connectors, power supply, jumpers configuration, software management, etc.) please, see technical manuals of the two modules contained in the couple.

In the tables are present some abbreviation and reference:

N.C. = Not Connected

N.M. = Not Mounted

*1 = to configure according to the performed connection.

GAB H844 connector. pin	GAB H844 signal name	GAB H844 configuration	ZC1 pin	GMM AM32 pin	GMM AM32 configuration	GMM AM32 signal name	Using on GMM AM32
CN1: Connector for relays outputs							
CN1.1	OUT A1	-	15	21	-	PA2 , ADC2	-
CN1.2	COMMON A	-	-	-	-	-	-
CN1.3	OUT A2	-	13	19	-	PA3 , ADC3	-
CN1.4	OUT B1	-	12	18	-	PB7 , SCK	DL2
CN1.5	OUT B2	-	11	17	-	PD7 , OC2	-
CN1.6	COMMON B	-	-	-	-	-	-
CN3: Connector for optocoupled digital inputs							
CN3.1	IN1	J35 in 1-2	16	22	-	PB1 , T1	-
CN3.2	IN2	J36 in 1-2	17	23	-	PB0 , T0 , XCK	-
CN3.3	IN3	J37 in 1-2	18	24	-	PD3 , INT1	-
CN3.4	IN4	J38 in 1-2	19	25	-	PD2 , INT0	-
CN3.5	COM1	-	-	-	-	-	-
CN4: Connector for analog inputs							
CN4.1	AIN1	-	27	33	-	PA7 , ADC7	-
CN4.2	AIN2	-	26	32	-	PA0 , ADC0	-
CN4.3	AIN3	-	25	31	-	PA1 , ADC1	-
CN4.4	AIN4	-	10	16	-	PA6 , ADC6	-
CN4.5	AIN5	J31 in 1-2	23	29	-	PD6 , ICP1	-
CN4.6	AIN6	J32 in 1-2	22	28	-	PB4 , /SS	-
CN4.7	AIN7	J33 in 1-2	21	27	-	PB2 , AIN0 , INT2	-
CN4.8	AIN8	J34 in 1-2	20	26	-	PB3 , AIN1 , OC0	-
CN4.9	AGND	-	14	20	-	GND	-
-	Vref	J11 in 2-3	1	7	-	Vref	-

FIGURE 1: CONNECTION TABLE (1 OF 5)

GAB H844 connector. pin	GAB H844 signal name	GAB H844 configuration	ZC1 pin	GMM AM32 pin	GMM AM32 configuration	GMM AM32 signal name	Using on GMM AM32	
CN5: Connector for asynchronous serial line in RS 232								
CN5.1	+5 VdcF	-	28	34	-	+Vdc POW	-	
CN5.2	-	J10 in 2-3	-	-	-	-	-	
CN5.3	TX RS232	J1, J9 N.C. J2, J3, J4 in 2-3 IC1, 2, 3, 4=N.M.	4	10	DSW1.1,2,3 ON DSW1.4,5 OFF	TXD RS232, TXD TTL, PDO	-	
CN5.4	-		-	-		-	-	-
CN5.5	RX RS232		3	9		RXD RS232, RXD TTL, PD1	-	
CN5.6	-		-	-		-	-	-
CN5.7	GND	-	14	20	-	GND	-	
CN5.8	-	J11 in 2-3	-	-	-	-	-	
CN5: Connector for asynchronous serial line in TTL								
CN5.1	+5 VdcF	-	28	34	-	+Vdc POW	-	
CN5.2	-	J10 in 2-3	-	-	-	-	-	
CN5.3	TX TTL	J1, J9 N.C. J2, J3, J4 in 2-3 IC1, 2, 3, 4=N.M.	4	10	DSW1.1,2,3 ON DSW1.4,5 OFF	TXD RS232, TXD TTL, PDO	-	
CN5.4	-		-	-		-	-	-
CN5.5	RX TTL		3	9		RXD RS232, RXD TTL, PD1	-	
CN5.6	-		-	-		-	-	-
CN5.7	GND	-	14	20	-	GND	-	
CN5.8	-	J11 in 2-3	-	-	-	-	-	

FIGURE 2: CONNECTION TABLE (2 OF 5)



GAB H844 connector. pin	GAB H844 signal name	GAB H844 configuration	ZC1 pin	GMM AM32 pin	GMM AM32 configuration	GMM AM32 signal name	Using on GMM AM32
CN5: Connector for asynchronous serial line in RS 422							
CN5.1	+5 VdcF	-	28	34	-	+Vdc POW	-
CN5.2	-	J10 in 2-3	-	-	-	-	-
CN5.3	TX- RS422	J1, J9 *1	4	10	DSW1.1,2,3 OFF	TXD RS232, TXD TTL, PDO	-
CN5.4	TX+ RS422	J2, J3, J4 in 1-2 J5 in 2-3	3	9			
CN5.5	RX+ RS422	IC3, 4=N.M.			DSW1.4,5 ON	RXD RS232, RXD TTL, PD1	-
CN5.6	RX- RS422	IC1, 2=MAX 483					
CN5.7	GND	-	14	20	-	GND	-
CN5.8	-	J11 in 2-3	-	-	-	-	-
-	DIR	-	24	30	-	PD5, OC1A	-
CN5: Connector for asynchronous serial line in RS 485							
CN5.1	+5 VdcF	-	28	34	-	+Vdc POW	-
CN5.2	-	J10 in 2-3	-	-	-	-	-
CN5.3	-	J1, J9 *1	4	10	DSW1.1,2,3 OFF	TXD RS232, TXD TTL, PDO	-
CN5.4	-	J2, J3, J4 in 1-2 J5 in 1-2	3	9			
CN5.5	RXTX+ RS485	IC2, 3, 4=N.M.			DSW1.4,5 ON	RXD RS232, RXD TTL, PD1	-
CN5.6	RXTX- RS485	IC1=MAX 483					
CN5.7	GND	-	14	20	-	GND	-
CN5.8	-	J11 in 2-3	-	-	-	-	-
-	DIR	-	24	30	-	PD5, OC1A	-

FIGURE 3: CONNECTION TABLE (3 OF 5)

GAB H844 connector. pin	GAB H844 signal name	GAB H844 configuration	ZC1 pin	GMM AM32 pin	GMM AM32 configuration	GMM AM32 signal name	Using on GMM AM32
CN5: Connector for asynchronous serial line Current Loop							
CN5.1	+5 VdcF	-	28	34	-	+Vdc POW	-
CN5.2	-	J10 in 2-3	-	-	-	-	-
CN5.3	TX- C.L.	J1, J9 N.C.	4	10	DSW1.1,2,3 OFF	TXD RS232 , TXD TTL , PDO	-
CN5.4	TX+ C.L.	J2, J3, J4 in 1-2 IC1, 2=N.M.	3	9	DSW1.4,5 ON	RXD RS232 , RXD TTL , PD1	-
CN5.5	RX+ C.L.	IC3=HP 4100	14	20	-	GND	-
CN5.6	RX- C.L.	IC4=HP 4200	-	-	-	-	-
CN5.7	GND	-	-	-	-	-	-
CN5.8	-	J11 in 2-3	-	-	-	-	-
CN6: Connector for multifunction signals, CAN, etc.							
CN6.1	+5 Vdc	-	28	34	-	+Vdc POW	-
CN6.2	MM PIN 21	J33 in 2-3	21	27	-	PB2 , AIN0 , INT2	-
CN6.3	MM PIN 8	J8 N.C	8	14	-	PB5 , MOSI	-
CN6.4	/INTRTC	-	5	11	-	/INTRTC	RTC+SRAM
CN6.5	MM PIN 9	J8 N.C.	9	15	-	PB6 , MISO	-
CN6.6	MM PIN 23	J31 in 2-3	23	29	-	PD6 , ICPI	-
CN6.7	GND	-	14	20	-	GND	-
CN6.8	MM PIN 22	J32 in 2-3	22	28	-	PB4 , /SS	-
CN7: Connector for USB interface -> NOT AVAILABLE							

FIGURE 4: CONNECTION TABLE (4 OF 5)



GAB H844 connector. pin	GAB H844 signal name	GAB H844 configuration	ZC1 pin	GMM AM32 pin	GMM AM32 configuration	GMM AM32 signal name	Using on GMM AM32
CN8: Connector for I2C BUS line							
CN8.1	+5 Vdc	-	28	34	-	+Vdc POW	-
CN8.2	SCL	-	6	12	-	PC0, SCL	RTC+SRAM
CN8.3	SDA	-	7	13	-	PC1, SDA	RTC+SRAM
CN8.4	GND	-	14	20	-	GND	-
CN9: Connector for multifunction signals, TTL inputs							
CN9.1	+5 Vdc	-	28	34	-	+Vdc POW	-
CN9.2	IN1 AUX	J35 in 2-3	16	22	-	PB1, T1	-
CN9.3	IN2 AUX	J36 in 2-3	17	23	-	PB0, T0, XCK	-
CN9.4	IN3 AUX	J37 in 2-3	18	24	-	PD3, INT1	-
CN9.5	IN4 AUX	J38 in 2-3	19	25	-	PD2, INT0	-
CN9.6	N.C.	-	-	-	-	-	-
CN9.7	GND	-	14	20	-	GND	-
CN9.8	MM PIN 20	J34 in 2-3	20	26	-	PB3, AIN1, OC0	-

FIGURE 5: CONNECTION TABLE (5 OF 5)